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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/590,644	07/25/2008	Syuji Kimura	SI08-P06576US	1496
33356	7590	01/06/2011	EXAMINER	
SoCAL IP LAW GROUP LLP 310 N. WESTLAKE BLVD. STE 120 WESTLAKE VILLAGE, CA 91362				KINKEAD, ARNOLD M
ART UNIT		PAPER NUMBER		
		2817		
NOTIFICATION DATE			DELIVERY MODE	
01/06/2011			ELECTRONIC	

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

USPTO@socalip.com

Office Action Summary	Application No.	Applicant(s)	
	10/590,644	KIMURA ET AL.	
	Examiner	Art Unit	
	Arnold Kinkead	2817	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1) Responsive to communication(s) filed on 11 August 2010.

2a) This action is **FINAL**. 2b) This action is non-final.

3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4) Claim(s) 1-5 is/are pending in the application.

4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5) Claim(s) _____ is/are allowed.

6) Claim(s) 1 and 2 is/are rejected.

7) Claim(s) 3-5 is/are objected to.

8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9) The specification is objected to by the Examiner.

10) The drawing(s) filed on 11 August 2010 is/are: a) accepted or b) objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a) All b) Some * c) None of:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) <input type="checkbox"/> Notice of References Cited (PTO-892)	4) <input type="checkbox"/> Interview Summary (PTO-413)
2) <input checked="" type="checkbox"/> Notice of Draftsman's Patent Drawing Review (PTO-948)	Paper No(s)/Mail Date. _____ .
3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08)	5) <input type="checkbox"/> Notice of Informal Patent Application
Paper No(s)/Mail Date _____.	6) <input type="checkbox"/> Other: _____

Response to Arguments

Applicant's arguments filed 08-11-10 have been fully considered but they are not persuasive. The examiner has considered applicants' concerns about the controller not switching at a predetermined time for disable/enable the phase difference...however, as noted in col. col 4, lines 30-45, :

"A controller 316 generates a digital open loop frequency control signal. The output of DAC 312 is connected to terminal 314, having DC tuning voltage potential thereon. The DAC 312, under the control of the digital open loop frequency control signal at input 311, produces a variable DC reference potential at terminal 314. The tuning voltage at terminal 314 is the tuning voltage for the expected frequency to which the PLL will lock."

The examiner is relying on this for reading broadly the switching(generating) of an enable(close loop) or disable(open loop) control via the controller.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1 and 2 are rejected under 35 U.S.C. 102(b) as being anticipated by Black et al(US 6,157,271 of record).

The reference by Black et al discloses a PLL, see figure 3, with VCO(114), loop filter(112), controller (316), phase comparator(102), a resistor(208) is shown in figure 3. A predetermined boost/supply signal is used to allow rapid stabilization of the oscillator as the PLL is disabled(open). A frequency divider (118) is shown. When the applied potential is set to the loop the phase detector and inherent pump current is not applied and thus is at high impedance (digital open loop).

Please see col. 4, lines 30-45 : "A controller 316 generates a digital open loop frequency control signal. The output of DAC 312 is connected to terminal 314, having DC tuning voltage potential thereon. The DAC 312, under the control of the digital open loop frequency control signal at input 311, produces a variable DC reference potential at terminal 314. The tuning voltage at terminal 314 is the tuning voltage for the expected frequency to which the PLL will lock."

Conclusion

THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of

the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Allowable Subject Matter

Claims 3-5 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Arnold Kinkead whose telephone number is 571-272-1763. The examiner can normally be reached on Mon-Thurs:8:00am-6:30pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Pascal can be reached on 571-272-1769. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

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/Arnold Kinkead/
Primary Examiner, Art Unit 2817